

## **General information**

ARM
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Area of interest	Y or N
CIP-01-2016-2017: <b>Telecom</b>	Y/N
CIP-01-2016-2017: <b>Health</b>	Y/N
CIP-01-2016-2017: Finance	Y/N
SEC-10-FCT-2017	Y/N

## **ARM**

## **Competencies**

Enable innovation at various (all?) level in the industry

200+ engineers in France

Underlaying technologies enabling security at HW & SW level

- IP bloc HW/SW (CPU-GPU-System-Video-Neuronal-Security... SW...
- Deployed from end points to Infrastructures
- Smart Card-Secure Elements-Terminals-Compute nodes-BDA-HPC

We have track of record in Eu project directly or in supporting partners involved.

2 support options

- ASSPs available from the partnership (Trustzone, TEE, CryptoCell, SecureCore...)
- Jointly specify & develop the right IPs for the industry
  - Itar free

Workshop june 15th Saint Quentin en Yvelines (contact me for details)

From concept to deployment: How to design secure connected low power ARM SoCs